

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
18 December 2003 (18.12.2003)

PCT

(10) International Publication Number
WO 2003/104992 A3

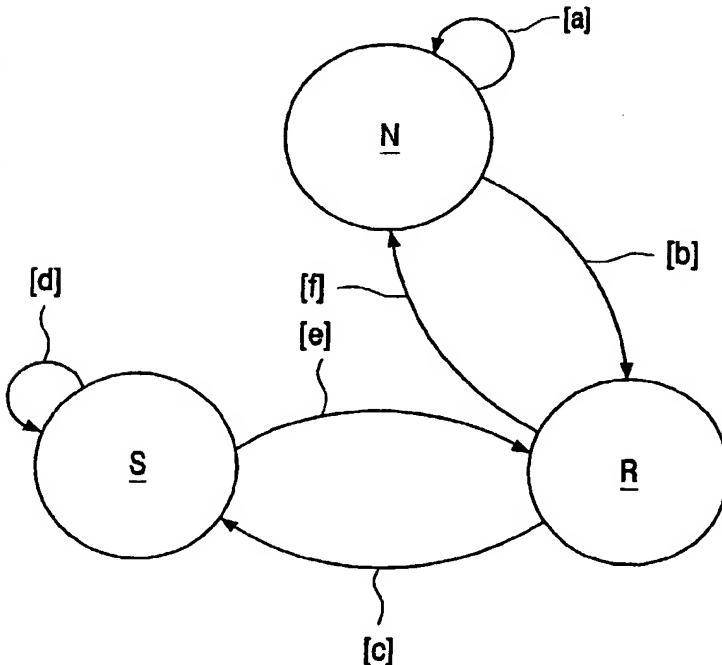
- (51) International Patent Classification⁷: **G06F 11/00**
- (21) International Application Number:
PCT/IB2003/002138
- (22) International Filing Date: **5 June 2003 (05.06.2003)**
- (25) Filing Language: **English**
- (26) Publication Language: **English**
- (30) Priority Data:
102 25 971.2 11 June 2002 (11.06.2002) DE
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

[Continued on next page]

(54) Title: METHOD AND BASE CHIP FOR MONITORING THE OPERATION OF A MICROCONTROLLER UNIT



(57) Abstract: To enable a method and a base chip for monitoring, by means of at least one base chip, the operation of at least one microcontroller unit that is intended for at least one application and is associated with a system to be further developed in such a way a reset of the microcontroller unit only takes place under defined conditions, it is proposed that a reset (R) of the microcontroller unit is caused if at least one special sequence, and particularly at least one drive or access sequence assigned to the reset operation (R), is applied to the base chip.



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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(88) Date of publication of the international search report:

11 March 2004

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F11/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

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Y	figure 2 column 1, line 3-12 column 4, line 55 -column 5, line 14 ---	2,3,10
Y	US 6 144 887 A (ABETA TAKEHIRO ET AL) 7 November 2000 (2000-11-07) abstract column 1, line 27-42 column 1, line 64 -column 2, line 2 column 8, line 15-26 column 13, line 43-56 column 14, line 16-39 column 15, line 1-11 ---	2,3,10
A	--- -/-	1,4-9

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Patent family members are listed in annex.

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Date of the actual completion of the international search

24 November 2003

Date of mailing of the international search report

08/01/2004

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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